

N THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Akihisa SHIMOMURA et al.

Serial No. 10/607,542

Filed: June 27, 2003

For: METHOD OF MANUFACTURING A

SEMICONDUCTOR DEVICE

Group Art Unit: 2812

Examiner: Stanetta D. Isaac

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class

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INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing references known to Applicant and requests that these references be made of record in the above identified application. Copies of the references listed are submitted herewith in accordance with 37 C.F.R. 1.98(a).

The requisite \$1,80.00 fee is enclosed.

Respectfully submitted,

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Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number. Complete if Known Substitute for form 1449A/PTO 10/607,542 **Application Number** INFORMATION DISC June 27, 2003 Filing Date STATEMENT BY APPI Akihisa SHIMOMURA et al. (use as many sheets as helestory 8 2006 First Named Inventor 2812 Group Art Unit Stanetta Isaac **Examiner Name** 0756-7171 Sheet Attorney Docket Number

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^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.